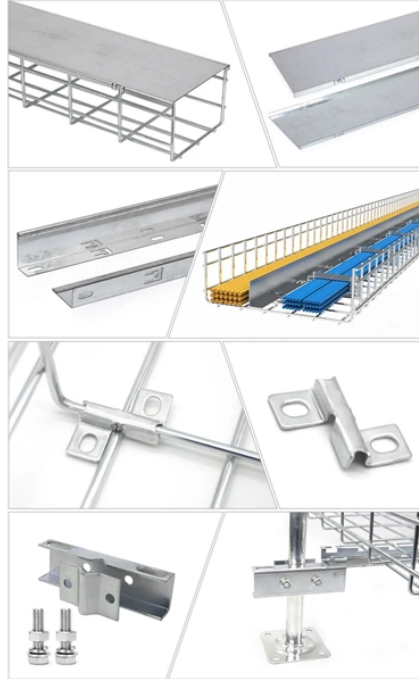


Optical module CPU chip



Optical module CPU chip



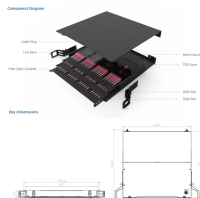
Intel recently developed an optical chip interconnect system and demonstrated its first fully integrated optical I/O (OCI) chiplet. This chiplet addresses one of the biggest challenges facing ...



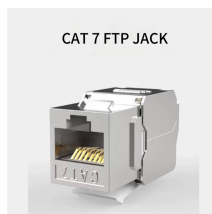
MALTA, N.Y., May 5, 2026 — GlobalFoundries (GF) has introduced an optical module solution for co-packaged optics (CPO). According to the company, the Silicon photonics Co ...



We refer to this approach as Co-Packaged Optics (CPO) when applied to networking applications and Optical Compute Interconnect (OCI) when applied to compute fabrics



Intel Corporation's Integrated Photonics Solutions (IPS) Group has demonstrated the industry's first fully integrated bidirectional optical compute interconnect (OCI) chiplet co-packaged ...



First demonstrated at OFC 2024, the industry-first Marvell 3D SiPho Engine - which supports 200Gbps electrical and optical interfaces - is a ...



Intel showed off a pretty cool piece of technology integrating an optical I/O chiplet with a CPU. The first iteration of the design is a fully integrated chiplet.



Intel paired one of its CPUs with an optical compute interconnect (OCI) chiplet.



The OCI chiplet combines a silicon photonics IC, which includes on-chip lasers and optical amplifiers, with an electrical IC. While the chiplet demonstrated was co-packaged with an Intel ...



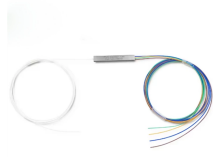
Meanwhile, the optical module, enabled by silicon photonics, is now treated similarly to electronic chips, and advanced co-packaged optics (CPO) is being extensively researched and ...



Intel recently developed an optical chip interconnect system and demonstrated its first fully integrated optical I/O (OCI) chiplet. This chiplet ...



World-first 16-wavelength bidirectional optical DWDM Link on a strand of standard single mode fiber. This Lightmatter breakthrough represents an 8X leap in bidirectional fiber bandwidth density and a ...



First demonstrated at OFC 2024, the industry-first Marvell 3D SiPho Engine - which supports 200Gbps electrical and optical interfaces - is a fundamental building block for incorporating ...

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